

FIG.1

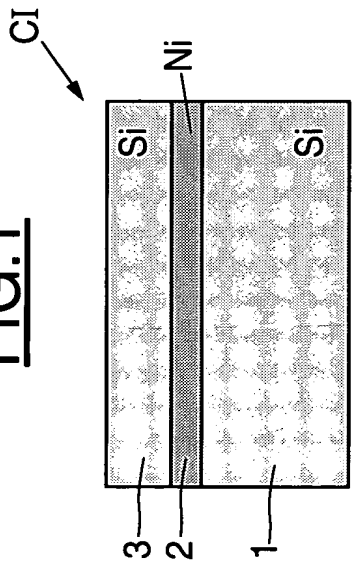


FIG.2

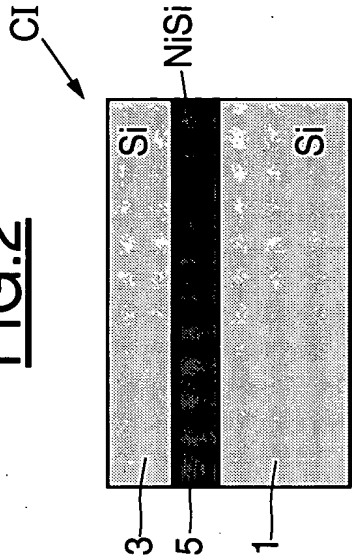


FIG.3

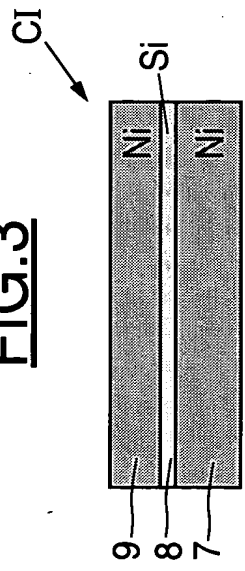


FIG.4

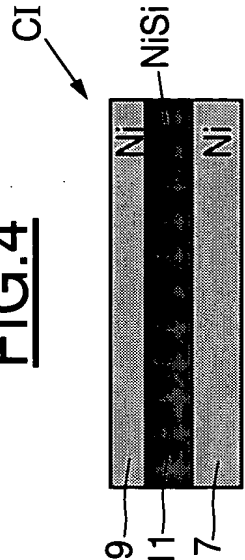


FIG. 5

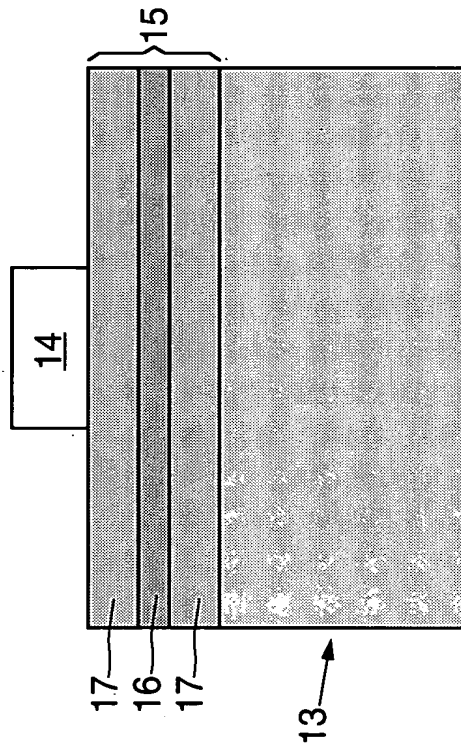


FIG.6

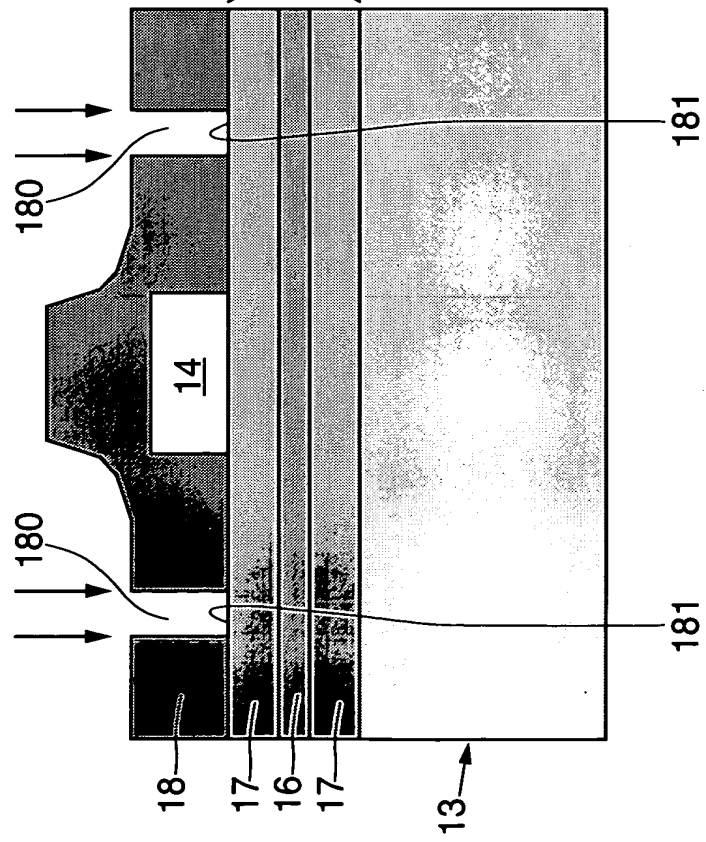


FIG.7

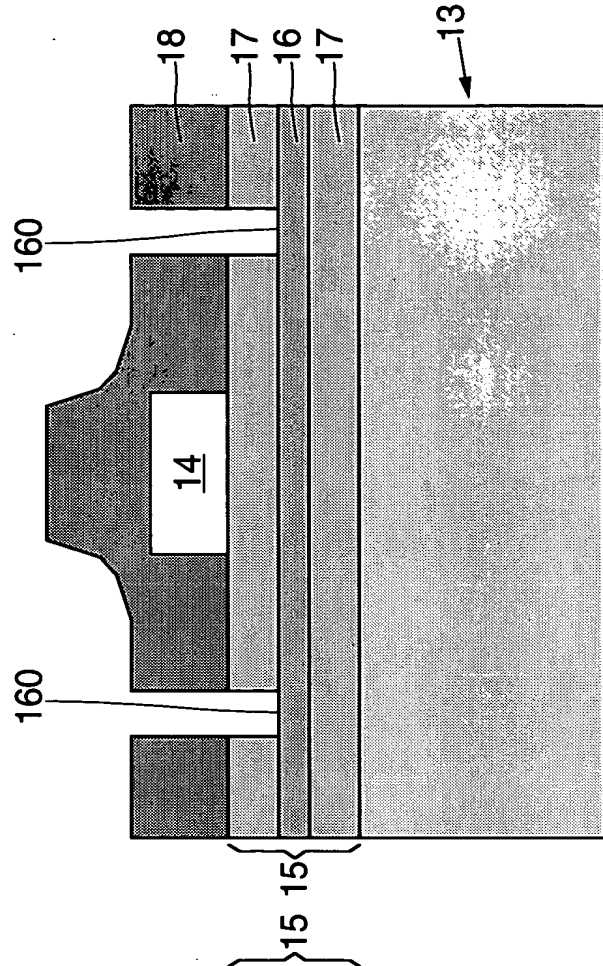


FIG. 8

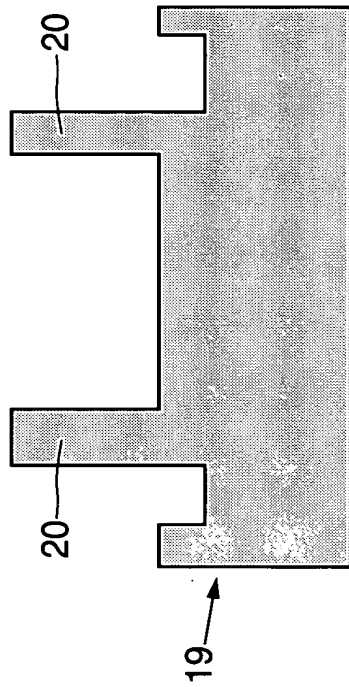


FIG. 9

FAR

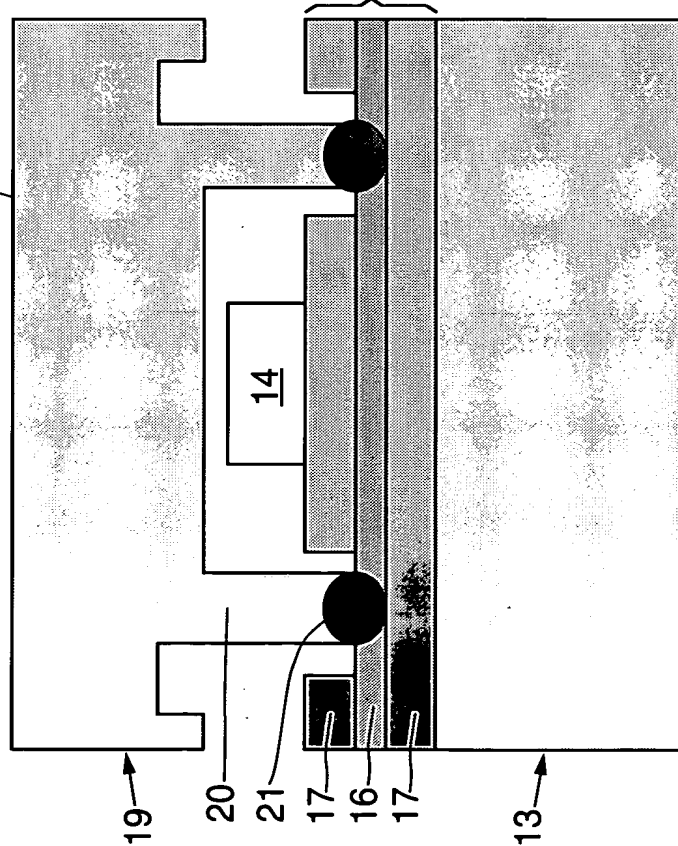


FIG. 10

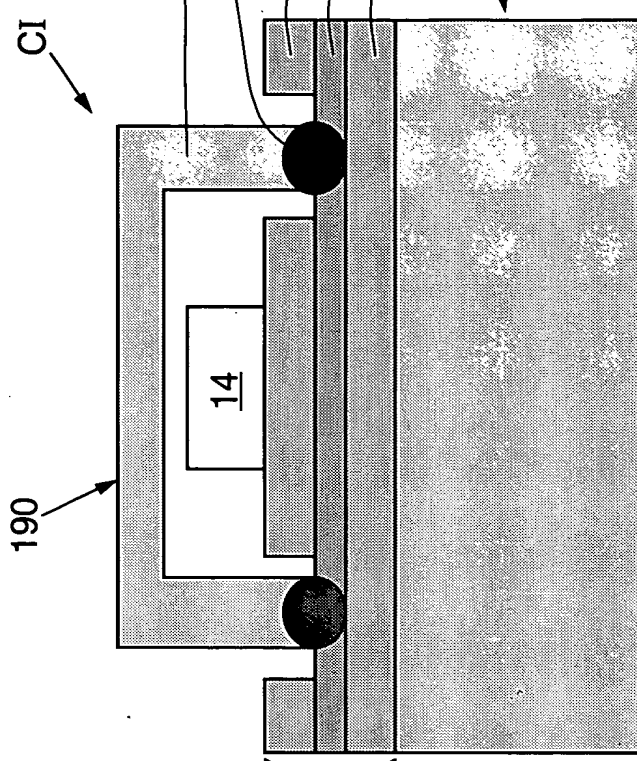


FIG.11

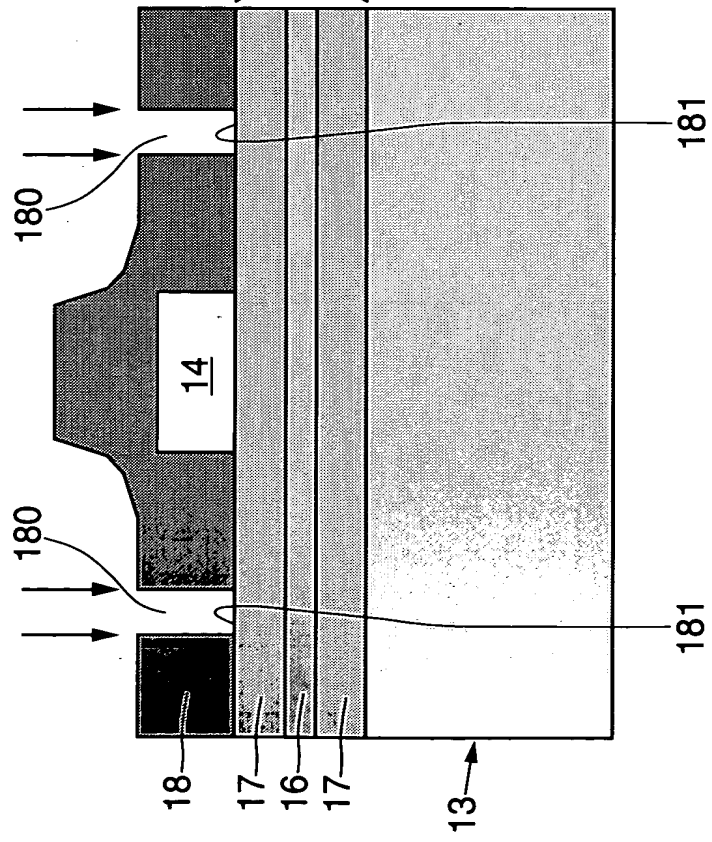


FIG.12

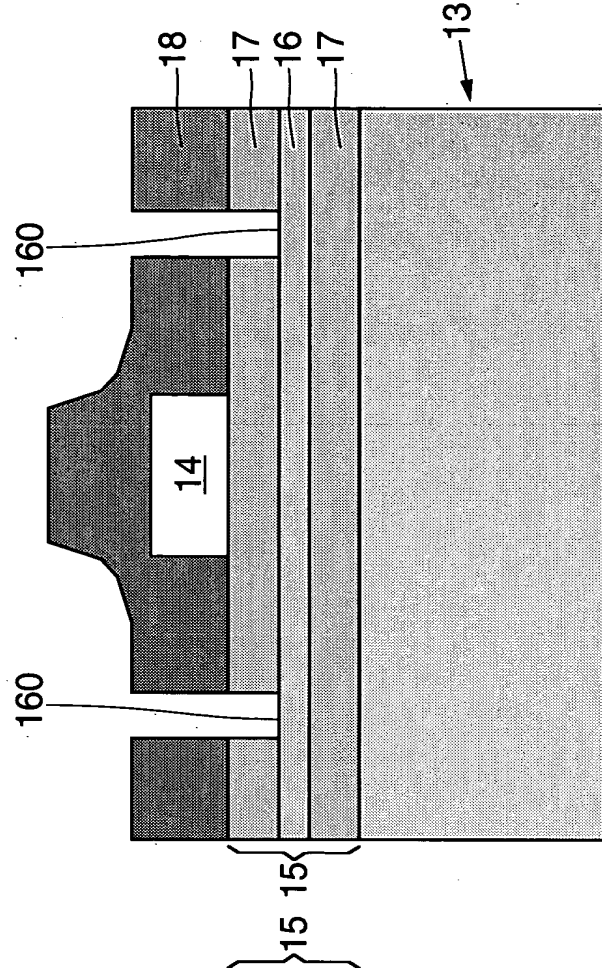
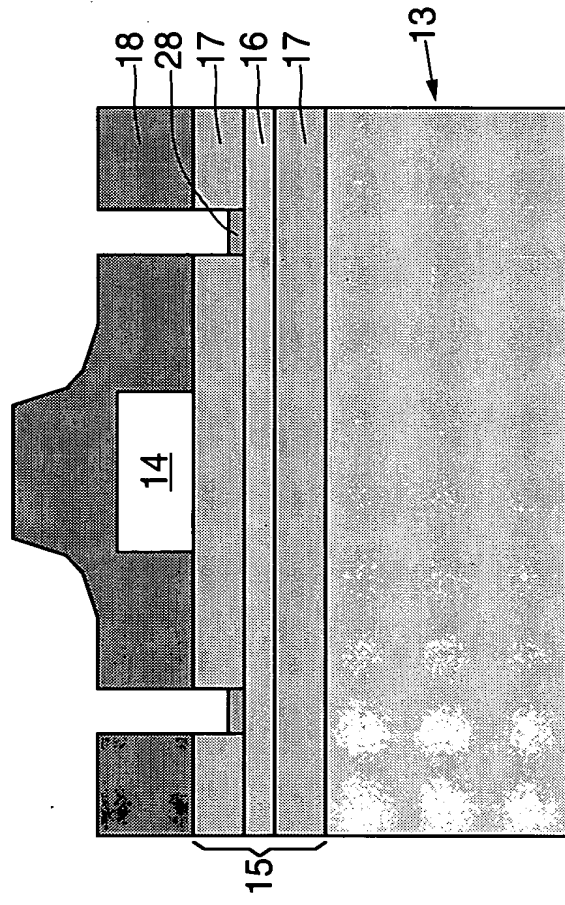


FIG.13



A cross-sectional view of a semiconductor device. A central channel region 14 is defined by gate regions 16. Source and drain regions 17, 18, 19, 20, and 21 are formed on the channel. The device is shown in a cross-section with a central channel 14, gate regions 16, and source/drain regions 17, 18, 19, 20, and 21.

[illegible]